ERICSSON

KRETSSCHEMA
CIRCUIT DIAGRAM

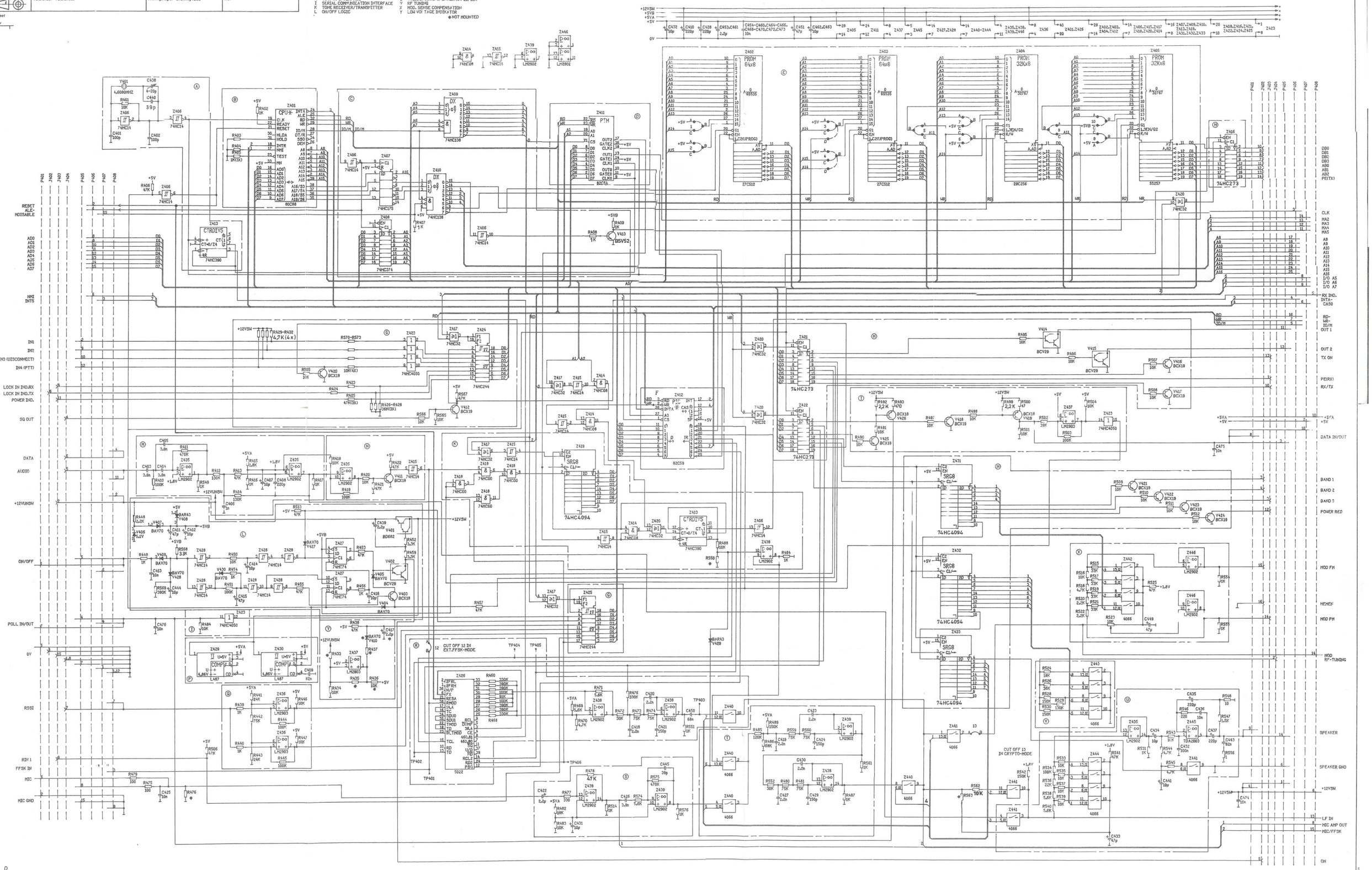
Blad - Sheet

Uppgjord - Prepared: KI/ERA/G/TM M.F.	Faktaansvarig - Subject responsible: F/TMB P.W.	Nr - No.: 1911-ROA 119 7528UEN	
Dokansv/Godk - Doc respons/Approved: F/TMBC Lars Brabberg		Datum - Date: 1990 06 13	Rev: S
Benämning: CPU-KORT		Title: CPU BOARD	File: FFF729
Toleranser - Tolerances		Ritningsregler - Drawing rules	Ref

Sheet
Rev

A OSC/FREQ.DIVIDER
B CPU
C ADDRESSDECODER
D TIMER
E MEMORY
F INTERRUPT CONTROLLER
G INPUT INTERFACE
H OUTPUT INTERFACE
I SERIAL COMMUNICATION INTERFACE
K TONE RECEIVER/TRANSMITTER
L ON/OFF LOGIC
M HP/F/F-FILTER
N SCHMITT TRIGGER
P 5V REGULATOR
Q RSSI DECODER
R MODEM
S MIC.AMPLIFIER/LIMITER
T I/F-FILTER
U VOL.CONTROL/SPEAKER AMPLIFIER
V RF TUNING
X MOD. SENSE COMPENSATION
Y LOW VOLTAGE INDICATOR
* NOT MOUNTED

Z402 27C512 ⇒ 1B-1C,2B-2C — 27C256,27C128,27C64 ⇒ 1B-1C,2A-2B
Z403 27C512 ⇒ 3B-3C,4B-4C — 27C256,27C128,27C64 ⇒ 3B-3C,4A-4B — 8KRAM/EEPROM ⇒ 3B-3D,4X-4X — 32KRAM/EEPROM ⇒ 3B-3D,4B-4D
Z404 2KRAM/EEPROM ⇒ 5A-5B,6X-6X,7B-7C — 8KRAM/EEPROM ⇒ 5B-5C,6B-6C,7B-7C — 32KRAM/EEPROM ⇒ 5B-5C,6B-6C,7A-7B
Z405 2KRAM/EEPROM ⇒ 8A-8B,9X-9X,10B-10C — 8KRAM/EEPROM ⇒ 8B-8C,9B-9C,10B-10C — 32KRAM/EEPROM ⇒ 8B-8C,9B-9C,10A-10B



Bildkort
01234
56789
Reprod

LZF 03085/F A1L RB

Nr - No. 1911-ROA 119 7528UEN Blad - Sheet 1 (1)